



Rayat Shikshan Sanstha's

# Yashwantrao Chavan Institute of Science, Satara (Autonomous)



(Lead College, Karmaveer Bhaurao Patil University, Satara, Maharashtra, India)

NAAC Reaccredited at A+ Grade with CGPA: 3.57, UGC-CPE-II, DBT-STAR, DST-FIST, RUSA Excellence,  
UGC-PARAMARSH, ISO: 9001-2015

## "Emerging Trends and Challenges in Electronics and Computing for Sustainable Development"



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### ETCESD-2024

### 2<sup>nd</sup> International Conference

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## Department of Electronics & Computer Science

### 22 - 23 March, 2024

# Certificate

(1<sup>st</sup> Rank)

This is to certify that

Prof./Dr./Mr./Ms./Mrs. P. S. Jadhav  
of Vivekanand College, Kolhapur has

Participated / Presented Paper (Oral /Poster) Entitled  
FPGA Based Fuzzy logic Spindle Temperature  
Compensation Using Thermoelectric Cooler in the

**"Emerging Trends and Challenges in Electronics and  
Computing for Sustainable Development" (ETCESD-2024)**  
held at Department of Electronics & Computer Science,  
Yashwantrao Chavan Institute of Science, Satara  
(Autonomous), Maharashtra, India from 22 - 23 March  
2024.

Mr. S. D. Jadhav  
Convener, ETCESD-2024

Mr. S. K. Shinde  
Chairman, ETCESD-2024

Dr. B. T. Jadhav  
President- ETCESD-2024

ETCESD-2024