

Vivekanand College, Kolhapur (Empowered Autonomous)
Department of Electronics
Notice

Date: 13.03.2024

All the students of B.Sc. II Electronics are hereby informed that their internal examination for Semester IV will be conducted in offline mode as per given schedule.

Paper Code	Section title	Marks	Date	Time
DSC1005D	Operational Amplifier	15	21-03-2024	03:50 pm-04.40 pm
	Microcontroller 8051	15	22-03-2024	02:10 pm-03.00 pm



(Dr. C. B. Patil)
HEAD

DEPARTMENT OF ELECTRONICS
VIVEKANAND COLLEGE, KOLHAPUR
(EMPOWERED AUTONOMOUS)

Shri Swami Vivekanand Shikshan Sanstha's
Vivekanand College, Kolhapur (Empowered Autonomous)

Class: B.Sc.-II, Semester-IV,

Internal Examination (2023-24)

DSC-1005D: Microcontroller 8051

Date:-22/03/2024

Time: 02:10pm to 03:00pm

Marks: 15

Q. 1 Select correct alternative for the following:

[3 x 1 = 3]

- 8051 has -----number of I/O 8 bit ports.
A) 8 B) 32 C) 4 D) 1
- The range of short jump instruction is _____.
A) 0 to 255 B) -128 to 127 C) 0 to 2K D) 0 to 65535
- _____ instruction has Register addressing mode.
A) MOV A, R7 B) MOV A,@R1 C) ADD A, #45H D) ADD A,48H

Q. Solve any ONE out of TWO:

[1 x 8 = 8]

- Explain the RAM and ROM structure/organization of 8051.
- Classify the instruction set according to functions and explain each with two suitable examples.

Q. Solve any ONE out of TWO:

[1 x 4 = 4]

- Explain the PSW register of 8051.
- Explain the DPTR register of 8051.