


Vivekanand College, Kolhapur (Empowered Autonomous)
Department of Electronics
Notice

Date: 20.10.2023

All the students of B.Sc. II Electronics are hereby informed that their internal examination for Semester III will be conducted in offline mode as per given schedule.

Paper Code	Section title	Marks	Date	Time
DSC1005C	Electronics Communication	15	26-10-2023	03:50 pm-04.40 pm
	Microprocessor 8085	15	27-10-2023	02:10 pm-03.00 pm


(Dr. C. B. Patil)

HEAD
DEPARTMENT OF ELECTRONICS
VIVEKANAND COLLEGE, KOLHAPUR
(EMPOWERED AUTONOMOUS)

Shri Swami Vivekanand Shikshan Sanstha's
Vivekanand College, Kolhapur (Empowered Autonomous)

Class: B.Sc.-II, Semester **III**, Internal Examination (2023-24)

Paper code: DSC-1005C

Section II: Microprocessor 8085

Date:-27/10/2023

Time: 2:10pm to 3:00pm

Marks: 15

Q. 1 Select correct alternative for the following:

[3 x 1 = 3]

- 9
- i) To interface IC 27128 with 8085, it requires address lines.
a) 11 b) 12 c) 13 d) 14
- ii) 8085 microprocessor can access bytes of memory.
a) 8 K b) 16K c) 32K d) 64K
- iii) 8085 microprocessor has number of General purpose registers.
a) 4 b) 6 c) 8 d) 10

Q. Solve any THREE:

[3 x 4 = 12]

- 23
- i. What are semiconductor memories? Explain in EPROM.
- ii. Draw the neat schematic showing the interface between 8085 microprocessor and 2764 EPROM. And write only the memory map.
- iii. Explain in brief the Flag register.
- iv. How AD0-AD7 bus is demultiplexed?

