## Vivekanand College, Kolhapur (Empowered Autonomous) Department of Electronics Notice

Date: 20.10.2023

All the students of B.Sc. II Electronics are hereby informed that their internal examination for Semester III will be conducted in offline mode as per given schedule.

Paper Code	Section title	Marks	Date	Time	
	Electronics Communication	15	26-10-2023	03:50 pm-04.40 pm	
DSC1005C			*		
	Microprocessor 8085	15	27-10-2023	02:10 pm-03.00 pm	

Eatl (Dr. C. B. Patil)

HEAD DEPARTMENT OF ELECTRONICS VIVERANAND COLLEGE, KOLHAPUR (EMPOWERED AUTONOMOUS)

1100 1104

# Vivekanand College, Kolhapur

(Empowered Autonomous) B.Sc. Part- II (Electronics) (Sem-III)

Internal Examination Oct-2023 Course Code: DSC - 1005 C

### Section-I: Electronic Communication

Marks: 15

[3 Marks]

1. A	ATM machine is example of communication.					
	A)	Full duplex	B)	Half duplex		
	C)	Simplex	D)	Both A and B		
2. E	Demodulation is done in					
	A)	Transmitter	B)	Radio receiver		
	C)	Between transmitter and receiver	D)	None of these		
3. II	In Amplitude Modulation, modulation index is					
	A)	Vm/Vc	B)	Vc/Vm		
	C)	Fm/Fc	D)	Vm/Fc		

2. Define the noise. What is mean by external noise? Describe its different types.

3. Explain necessity of modulation in wireless communication.

4. Define Demodulation. Explain the working of AM diode detector .

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Date: 26/10/2023

#### [12 Marks]

## Shri Swami Vivekanand Shikshan Sanstha's Vivekanand College, Kolhapur (Empowered Autonomous) Class: B.Sc.-II, Semester M. Internal Examination (2023-24)

Paper code: DSC-1005C

Section II: Microprocessor 8085

	7/10/2023					
Time: 2	Marks: 15					
Q. 1 Se	lect correct alt	ernative for t	he following:			[3 x 1 = 3]
i)	To interface l					
	a) 11 💌 👘	b) 12	c) 13	d) 14		
ii) 🖌						
	a) 8 K	b) 16K	c) 32K	d) 64K		
iii)	8085 microprocessor has number of General purpose registers.					1
	a) 4	b) 6	c) 8	d) 10		

#### Q. Solve any THREE:

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- i. What are semiconductor memories? Explain in EPROM.
- Draw the neat schematic showing the interface between 8085 microprocessor and 2764 EPROM. And write only the memory map.

 $[3 \times 4 = 12]$ 

- iii. Explain in brief the Flag register.
- iv. How AD0-AD7 bus is demultiplexed?

