

Vivekanand College, Kolhapur (Empowered Autonomous)
Department of Electronics
Notice

Date: 12.03.2024

All the **Major-Minor** students of B.Sc. I Electronics are hereby informed that their internal examination for Semester II will be conducted in offline mode as per given schedule.

Paper Code	Section title	Marks	Date	Time
DSC03ELE22/ MIN03ELE22	Digital Electronics II	10	26-03-2024	01:00 pm-02.00 pm
DSC03ELE21/ MIN03ELE22	Analog Electronics II	10	27-03-2024	12:20 pm-1.10 pm



(Dr. C.B. Patil)

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DEPARTMENT OF ELECTRONICS
VIVEKANAND COLLEGE, KOLHAPUR
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VIVEKANAND COLLEGE, KOLHAPUR (EMPOWERED AUTONOMOUS)

Class: B.Sc.-I, Semester-II, Internal Examination (2023-24)

Course code: DSC03ELE22 - MIN03ELE22

Subject: Digital Electronics

Time: 01:10 pm to 02:00 pm

Marks: 10

Date:-26/03/2024

Q.1. Select the Correct alternative

[2M]

1. If an S-R latch has a 1 on the S input and a 0 on the R input the latch will be _____
a) set b) reset c) invalid d) Clear
2. Positive edge-triggered flip-flop changes its state when _____
a) Low-to-high transition of clock b) High-to-low transition of clock
c) Enable input (EN) is set d) Preset input (PRE) is set

Q.2 .Attempt any Two (Four mark each)

[8 M]

1. Explain working of RS Latch using NOR gates
2. Explain working of D flip-flop with suitable diagram and truth table.
3. Explain the race around condition. How it is avoided?
4. Describe the working of JK flip flop with circuit diagram and truth table


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Notice

Date: 15.03.2024

All the **Open Elective** students of B.Sc. I Electronics are hereby informed that their internal examination for Semester II will be conducted in offline mode as per given schedule.

Paper Code	Section title	Marks	Date	Time
OEC03ELE21	Circuits Fundamentals-II	10	23-03-2024	01:00 pm-01.30 pm
OEC03ELE22	Semiconductor Devices and Circuits	10	23-03-2024	01:30 pm-02.00 pm



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Seat No.

Ques. paper code

VIVEKANAND COLLEGE, KOLHAPUR (EMPOWERED AUTONOMOUS)

B.Sc. Part- I (Electronics) (SEM-I) Examination:

Course Code and Name: OEC03ELE21: Circuit Fundamentals-II

Day: Saturday

Time: 12.00 pm to 12.30 pm

Date: 23/03/2024

Marks: 10

Q. 1. Select correct alternative

[1*4=4]

1. Unit of capacitors capacitance is.....
a) Farad b) volt c) ampere d) ohm
2. Resistance for brown black yellow
a) 10k b) 100k c) 2k d) 3k
3. In carbon composition resistor the last colour band of silver indicates % tolerance
a) ± 5 b) ± 10 c) ± 20 d) ± 40
4. Electrically operated switch is
a) Transformer b) Relay c) Fuse d) Inductor

Q. 2 Solve any two

[2*3=6]

1. Give the classification of Resister.
2. Explain color code system of resister
3. What is switch Explain in detail
4. Explain with suitable diagram of Electromagnetic relay

Seat No.

Ques. paper code

VIVEKANAND COLLEGE, KOLHAPUR (EMPOWERED AUTONOMOUS)

B.Sc. Part- I (Electronics) (SEM-I) Examination:

Course Code and Name: **OE03ELE22**: Semiconductor devices-II

Day: Saturday

Time: 12.30 pm to 01.00 pm

Date: 23/03/2024

Marks: 10

Q.1) Select Correct Alternative.

04

i) Mostly used biasing method is _____ biased.

a) Fixed b) Collector to base c) Potential Divider d) none of these

ii) Operating point Q is shifted due to changes in _____.

a) Temperature b) V_{BE} & β c) both a & b d) none of these

iii) FET is _____ controlled device.

a) Current b) Voltage c) inductance d) none of these

iv) _____ of the following is Normally in OFF state.

a) D- MOSFET b) E- MOSFET c) JFET d) none of these

Q.2) Attempt any Two

6

1) What is biasing? Explain need of biasing

2) What is thermal Runway? Define stability factors.

3) Compare BJT and FET
